



2128

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

September 22, 2005

Applicant : Ioan Dancea  
Application/Control No. : 09/874,027  
Title : METHOD AND VLSI CIRCUITS ALLOWING  
TO CHANGE DYNAMICALLY THE LOGICAL BEHAVIOUR  
Art Unit : Simulation and Emulation, Art Unit 2128  
Examiner : FRED FERRIS, Patent Examiner  
U. S. Patent and Trademark Office  
Randolph Building, Room 5D19  
401 Dulany Street  
ALEXANDRIA, VA 22313  
U.S.A.

Dear Sir,

The applicant's response to the Examiner's action dated July 22, 2005 is re-submitted as a consequence of the Notice of Non-Compliant Amendment (37 CFR 1.121) dated of September 8, 2005. The individual status of each claim is now in compliance with the requirements of 37 CFR 1.121. The remarks to observations signalled by the examiner's last action are:

REMARKS

(1) As was mentioned in previous correspondence, the description of the expert system was eliminated. Consequently, in the section "Detailed Description of the Preferred Embodiments" (page 6) the paragraphs [0101] to [0108] must be eliminated. The amendment to the specification regarding the description of the Figures 10, 11 and 12, relating to the second embodiment of the proposed VLSI